

Docket No.: 43890-532

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Toshiaki TAKENAKA, et al.

Serial No.:

Group Art Unit:

Filed: August 14, 2001

Examiner:

*#6
Priority
Citation
2-01*

For: CIRCUIT BOARD AND METHOD OF MANUFACTURING SAME

**CLAIM OF PRIORITY AND
TRANSMITTAL OF CERTIFIED PRIORITY DOCUMENT**

Commissioner for Patents
Washington, DC 20231

Sir:

In accordance with the provisions of 35 U.S.C. 119, Applicants hereby claim the priority of:

Japanese Patent Application No. 2000-247332,
Filed August 17, 2000

A certified copy is submitted herewith.

Respectfully submitted,

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ARTIFACT SHEET

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11/15/02